



T2(16bit) or 2+3CK(32bit) = Max 7MHz
 T1CK(16bit) = Over 50MHz <==good

IC? PIC24FV32KA301			
1	RA5/MCLR/Vpp	AVdd/Vdd	20
2	RA0/AN0/SCK2/PGEC2	Vss/AVss	19
3	RA1/AN1/SD02/PGED2	RB15/AN9/T2CK/SS1	18
4	RB0/U2TX/AN2/SDI2	RB14/INT1/SDI1	17
5	RB1/U2RX/AN3	RB13/AN11/SDO1	16
6	RB2/AN4/SDA2/U1RX	RB12/SCK1/SS2/INT2	15
7	RA2/OSCI/AN13/CLKI	Vcap	14
8	RA3/OSCO/AN14/CLKO	RB9/SDA1/T1CK	13
9	RB4/AN15/PGED3	RB8/SCL1	12
10	RA4/PGEC3	RB7/INT0/U1TX	11

2020/04/06:TIMER1, I2C2-Master, EXT-INT 0 1 Use
 1st 2020-03-23

Sheet: /
 File: BinaryCounter.sch

Title: PIC24FV32KA302 Binary Counter

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